

Class	Subclass
427	384

UTILITY SERIAL NUMBER 08/519,420	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 08/519,420	FILING DATE 12/30/96	CLASS 427	SUBCLASS 384	GROUP ART UNIT 1762	EXAMINER Pdgett
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APPLICANTS: TAKESHI FUKUNAGA, KANAGAWA, JAPAN; HISASHI OHTANI, KANAGAWA, JAPAN; AKIHARU MIYANAGA, KANAGAWA, JAPAN.

\*CONTINUING DATA\*\*\*\*\*  
 VERIFIED THIS APPLN IS A CON OF 08/519,420 08/25/95, *abandon*  
*4/23/01*

\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*  
 VERIFIED JAPAN 6-225851 08/26/94  
*4/23/01*

**CPA CPA**

*12/4/02  
RCE*

Foreign priority claimed as USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY JPX	SHEETS DRWGS. 10	TOTAL CLAIMS 21	INDEP. CLAIMS 8	FILING FEE RECEIVED \$1,192.00	ATTORNEY'S DOCKET NO. 0756-1614
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 2010 CORPORATE RIDGE  
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TITLE: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE, METHOD OF FABRICATING A SEMICONDUCTOR DEVICE UTILIZING A CATALYST MATERIAL SOLUTION  
 U.S. DEPT. OF COMM./PAT. & TM—PTO-436L (Rev.12-94)

PARTS OF APPLICATION FILED SEPARATELY				Applications Examiner	
NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg	Figs Drwg
				Print Fig	
Label Area		PREPARED FOR ISSUE		ISSUE BATCH NUMBER	
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